

COURSE – 7 Memory Devices and Microprocessors

Credits - 4

Paper code PHY553

BLOCK – I LOGIC FAMILIES

- Unit –1 : Logic families and their performance characteristics
- Unit –2 : Emitter Coupled Logic (ECL, PMOs, CMOs Logic and Tri state Logic)
- Unit –3 : Comparisons of Logic families

BLOCK – II SEMI CONDUCTOR MEMORIES

- Unit –4 : Classification and Characteristics of Memories
- Unit –5 : Memory organization and expansion

BLOCK – III INTELL 8085 MICROPROCESSOR ORGANIZATION AND ARCHITECTURE

- Unit –6 : Micro processor Organization and Architecture
- Unit –7 : Pin configuration of Intel 8085 Micro processor
- Unit –8 : Timing diagrams

BLOCK – IV ADDRESSING MODES AND PROGRAMMING OF 8085 MICRO PROCESSORS

- Unit –9 : Addressing modes and instruction set of Intel 8085
- Unit –10 : Programming of Micro processor Intel 8085
- Unit –11 : Assembly Language Programming using Loops

BLOCK – V PERIPHERAL DEVICES AND INTERFACING

- Unit –12 : I/O Interfacing & Data Transfer Schemes
- Unit –13 : Intel 8053 Programmable interval Timer
- Unit –14 : Programmable Peripheral Interface (8255)
- Unit –15 : Priority Interrupt Controller (8259)

BLOCK – VI INTEL 8086 MICROPROCESSOR

- Unit –16 : Intel 8086 Micro processor
- Unit –17 : Addressing Modes and Instruction set of Intel 8086 Micro Processor
- Unit –18 : Pin Configuration of Intel 8086 Micro Processor

BLOCK – VII ADVANCED MICRO PROCESSORS

- Unit –19 : Architecture of Micro Processors 80286, 80386, 8086
- Unit –20 : The Pentium Microprocessor